EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	50	("4985866" "4345319" "5231604" "6185709" "4254477" "4506364" "4598388" "5325332" "6122208" "6275963" "6314037" "6466490" "6665220" "6694448" "4319225" "4426685" "4947410" "5959883" "6233713" "5392292" "5406565" "5805604" "4415938" "4768181" "5394388" "5594693" "5623507" "6249465" "4896055" "5465053" "5479370" "5812465" "5968190" "6056669" "6373758" "4322816" "4546448" "5255013" "4454600" "5818364" "5359722" "5463701" "5502736" "5715276" "5881106" "5933447" "5942002" "3893072" "3812476" "4168513").pn.	US-PGPUB; USPAT	OR	ON	2006/09/01 09:16
L2	42	("654421" "6373775" "6314033" "6262935" "6188597" "6134159" "6021075" "6006311" "5958068" "5808944" "5699308" "5608685" "5574729" "5568444" "5508969" "5260902" "5255217" "5206831" "5146429" "5070502").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 10:39
L3	19	("654421" "6373775" "6314033" "6262935" "6188597" "6134159" "6021075" "6006311" "5958068" "5808944" "5699308" "5608685" "5574729" "5568444" "5508969" "5260902" "5255217" "5206831" "5146429" "5070502").pn.	USPAT	OR	ON	2006/09/01 11:03
L4	69	l1 or l3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 11:03
L5	13	l4 and (shift adj redund$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 11:06
L6	1	l4 and (consecutive near2 (error fail bad failed failing erroneous))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 11:08

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L7	0	I5 and I6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 11:05
L8	2	I4 and (consecutive near2 (error fail bad defect defective failed failing erroneous))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 11:07
L9	4	(shift near2 redund$) and (consecutive near2 (error fail bad defect defective failed failing erroneous))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 11:15
L10	143	(memory near2 (test testing tester tested)) and (shift scan) and (consecutive near2 (error fail bad failed failing erroneous))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 11:09
L11	143	I10 not I3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/01 11:15

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Inventor Name Search Result

Your Search was:

Last Name = WUU
First Name = JOHN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09084689	6282143	150	05/26/1998	MULTI-PORT STATIC RANDOM ACCESS MEMORY DESIGN FOR COLUMN INTERLEAVED ARRAYS	WUU, JOHN
09492510	6243287	150	01/27/2000	Distributed decode system and method for improving static random access memory (SRAM) density	WUU, JOHN
09790132	6366526	150	02/21/2001	Static random access memory (SRAM) array central global decoder system and method	WUU, JOHN
10133946	6580635	150	04/25/2002	BITLINE SPLITTER	WUU, JOHN
10298729	Not Issued	123	11/18/2002	System and method for generating a graphical representation of fault data of a memory device under test	WUU, JOHN
10600875	Not Issued	121	06/20/2003	Bias generation having adjustable range and resolution through metal programming	WUU, JOHN
10600878	Not Issued	95	06/20/2003	PROGRAMMABLE WEAK WRITE TEST MODE (PWWTM) BIAS GENERATION HAVING LOGIC HIGH OUTPUT DEFAULT MODE	WUU, JOHN
11017981	Not Issued	161	12/20/2004	Reducing power in SRAMs while maintaining cell stability	WUU, JOHN
11229084	Not Issued	30	09/16/2005	System and method for avoiding attempts to access a defective portion of memory	WUU, JOHN
10653020	Not Issued	71	08/30/2003	Shift redundancy encoding for use with digital memories	WUU, JOHN J.
11024086	7076376	150	12/28/2004	SYSTEM AND METHOD FOR CALIBRATING WEAK WRITE TEST MODE (WWTM)	WUU, JOHN J.
11234480	Not Issued	30	09/23/2005	Reduced bitline leakage current	WUU, JOHN J.
11395475	Not Issued	20	03/30/2006	Circuit supply voltage control using an error sensor	WUU, JOHN J.
09510287	6192001	150	02/21/2000	Integrated weak write test mode (WWWTM)	WUU, JOHN J.

Inventor Search Completed: No Records to Display.

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Your Search was:

Last Name = WEISS
First Name = DONALD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
60235309	Not Issued	159	09/26/2000	Train in a tree	WEISS JR., DONALD
06334098	Not Issued	161	01/08/1982	CONVERSION OF HIGH SPEED ANALOG DATA TO HIGH RESOLUTION DIGITAL DATA	WEISS, DONALD
06436022	4536851	150	10/22/1982	ELECTRONIC THERMOMETER AND ASSOCIATED APPARATUS	WEISS, DONALD
06567377	4631350	150	12/30/1983	LOW COST THERMOCOUPLE APPARATUS AND METHODS FOR FABRICATING THE SAME	WEISS, DONALD
06901687	4795498	150	08/29/1986	LOW COST THERMOCOUPLE APPARATUS AND METHODS FOR FABRICATING THE SAME	WEISS, DONALD
08400342	Not Issued	166	03/08/1995	FREQUENCY MODULATED CLOCK FOR REDUCED RADIATION	WEISS, DONALD
08637331	Not Issued	163	04/23/1996	FREQUENCY MODULATED CLOCK FOR REDUCED RADIATION	WEISS, DONALD
10649285	Not Issued	93	08/27/2003	METHOD AND APPARATUS FOR CONTROLLING FRICTION BETWEEN A FLUID AND A BODY	WEISS, DONALD
10716084	Not Issued	41	11/18/2003	Electrowetting battery having a nanostructured electrode surface	WEISS, DONALD
10766449	Not Issued	71	01/27/2004	Fuse for projected ordnance	WEISS, DONALD
10798064	Not Issued	30	03/11/2004	Drug delivery stent	WEISS, DONALD
10803565	Not Issued	30	03/18/2004	Reserve cell-array nanostructred battery	WEISS, DONALD
10803641	Not Issued	30	03/18/2004	Reversibly-activated nanostructured battery	WEISS, DONALD
10810774	Not Issued	41	03/26/2004	Nanostructured liquid bearing	WEISS, DONALD
10816569	7005593	150	04/01/2004	LIQUID ELECTRICAL MICROSWITCH	WEISS, DONALD
11379507	Not Issued	20	05/23/2006	LIQUID SWITCH	WEISS, DONALD

60497262	Not Issued	159	08/22/2003	Method and apparatus for reducing friction between a fluid and a body	WEISS, DONALD
06236474	4345251	250	02/20/1981	SOLID STATE COMMUTATOR SWITCH	WEISS, DONALD C.
06231388	4363749	150	02/04/1981	WATER CLARIFICATION COAGULANT ADSORBENT	WEISS, DONALD E.
06243903	Not Issued	161	02/24/1981	ZEOLITE CATALYSTS	WEISS, DONALD E.
07739370	Not Issued	166	08/02/1991	TECHNIQUE TO SUPPORT PROGRESSIVELY PROGRAMMABLE NONVOLATILE MEMORY	WEISS, DONALD G.
08210409	5390317	150	03/18/1994	TECHNIQUE TO SUPPORT PROGRESSIVELY PROGRAMMABLE NONVOLATILE MEMORY	WEISS, DONALD G.
06039672	4239435	150	05/16/1979	APPARATUS AND METHOD FOR ASSEMBLYING RANDOMLY ARRANGED ARTICLES	WEISS, DONALD J.
10997795	Not Issued	41	11/24/2004	Techniques for microchannel cooling	WEISS, DONALD P.
60280482	Not Issued	159	04/02/2001	Micromachined interferometric optical fiber sensor	WEISS, DONALD P.
06178444	Not Issued	161	08/15/1980	LIGHTING DEVICE	WEISS, DONALD P.
06178445	Not Issued	161	08/15/1980	PROTECTIVE BANNER FOR LIGHTING DEVICE	WEISS, DONALD P.
06178446	4339788	150	08/15/1980	LIGHTING DEVICE WITH DYNAMIC BULB POSITIONING	WEISS, DONALD P.
06178447	Not Issued	161	08/15/1980	METHOD OF PRODUCING NEW LIGHTING DEVICE	WEISS, DONALD P.
06420746	Not Issued	161	09/21/1982	CONVENIENCE FLASHLIGHT	WEISS, DONALD P.
06420747	Not Issued	164	09/21/1982	A PACKAGE OF FLASHLIGHTS	WEISS, DONALD P.
06480304	Not Issued	164	03/30/1983	PORTABLE DISPOSABLE LIGHTING DEVICE	WEISS, DONALD P.
07109632	Not Issued	164	10/19/1987	ULTRASONIC MEASURING DEVICE	WEISS, DONALD P.
07236106	Not Issued	169	08/24/1988	TIRE PRESSURE GAUGE	WEISS, DONALD P.
08278453	Not Issued	161	07/21/1994	SHOCK MONITORING DEVICE	WEISS, DONALD P.
08855091	5821595	150	05/13/1997	CARRIER STRUCTURE FOR TRANSDUCERS	WEISS, DONALD P.
60017751	Not Issued	159	05/15/1996	CARRIER STRUCTURE FOR SEMICONDUCTOR TRANSDUCERS	WEISS, DONALD P.
09505549	6550034	150	02/17/2000	BUILT-IN SELF TEST FOR CONTENT	WEISS, DONALD R

				ADDRESSABLE MEMORY	
09505561	6285579	150	02/17/2000	System and method for enabling/disabling sram banks for memory access	WEISS, DONALD R
09492510	6243287	150	01/27/2000	Distributed decode system and method for improving static random access memory (SRAM) density	WEISS, DONALD R.
09496714	6240009	150	02/02/2000	Asymmetric ram cell	WEISS, DONALD R.
09506620	6249465	150	02/18/2000	Redundancy programming using addressable scan paths to reduce the number of required fuses	WEISS, DONALD R.
09507333	6208565	150	02/18/2000	Multi-ported register structure utilizing a pulse write mechanism	WEISS, DONALD R.
09507498	6493855	150	02/18/2000	FLEXIBLE CACHE ARCHITECTURE USING MODULAR ARRAYS	WEISS, DONALD R.
09507862	6292093	150	02/22/2000	Multi-bit comparator	WEISS, DONALD R.
09790132	6366526	150	02/21/2001	Static random access memory (SRAM) array central global decoder system and method	WEISS, DONALD R.
09812659	6363006	150	03/19/2001	Asymmetric ram cell	WEISS, DONALD R.
09919091	Not Issued	161	07/31/2001	Data-shifting scheme for utilizing multiple redundant elements	WEISS, DONALD R.
10351234	Not Issued	41	01/23/2003	Method and circuit for reducing silent data corruption in storage arrays with no increase in read and write times	WEISS, DONALD R.
10600875	Not Issued	121	06/20/2003	Bias generation having adjustable range and resolution through metal programming	WEISS, DONALD R.

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Your Search was:

Last Name = WEISS
First Name = DONALD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10600878	Not Issued	95	06/20/2003	PROGRAMMABLE WEAK WRITE TEST MODE (PWWTM) BIAS GENERATION HAVING LOGIC HIGH OUTPUT DEFAULT MODE	WEISS, DONALD R.
10653020	Not Issued	71	08/30/2003	Shift redundancy encoding for use with digital memories	WEISS, DONALD R.
11017981	Not Issued	161	12/20/2004	Reducing power in SRAMs while maintaining cell stability	WEISS, DONALD R.
11024086	7076376	150	12/28/2004	SYSTEM AND METHOD FOR CALIBRATING WEAK WRITE TEST MODE (WWTM)	WEISS, DONALD R.
11183545	Not Issued	30	07/18/2005	Banked cache with multiplexer	WEISS, DONALD R.
11229084	Not Issued	30	09/16/2005	System and method for avoiding attempts to access a defective portion of memory	WEISS, DONALD R.
11234480	Not Issued	30	09/23/2005	Reduced bitline leakage current	WEISS, DONALD R.
08724204	5787041	150	10/01/1996	SYSTEM AND METHOD FOR IMPROVING A RANDOM ACCESS MEMORY (RAM)	WEISS, DONALD R.
09073670	5986923	150	05/06/1998	A METHOD AND APPARATUS FOR IMPROVING READ/WRITE STABILITY OF A SINGLE-PORT SRAM CELL	WEISS, DONALD R.
09507207	6226217	150	02/18/2000	Register structure with a dual-ended write mechanism	WEISS, DONALD R.
09510287	6192001	150	02/21/2000	Integrated weak write test mode (WWWTM)	WEISS, DONALD R.
09577925	Not Issued	161	05/24/2000	Playing card spot	WEISS, DONALD S.
09108765	6121249	150	07/01/1998	TREATMENT AND PREVENTION OF CARDIOVASCULAR DISEASES WITH HELP OF ASPIRIN, ANTIOXIDANTS, NIACIN, AND CERTAIN VITAMINS	WEISSMAN, DONALD L.
09393751	6323188	150	09/10/1999	TREATMENT AND PREVENTION OF CARDIOVASCULAR DISEASES,	WEISSMAN, DONALD L.

HEART ATTACK, AND STROKE,
PRIMARY AND SUBSEQUENT, WITH
HELP OF ASPIRIN AND CERTAIN
VITAMINS

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Last Name = UNANGST
First Name = MATTHEW

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10653020	Not Issued	71	08/30/2003	Shift redundancy encoding for use with digital memories	UNANGST, MATTHEW RYAN

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